

ABSTRACT OF DISCLOSURE

A layout check system checks whether a layout of a power source, a component including a power pin, and a bypass capacitor on a PCB and defined by layout data created using
5 a CAD system allows the bypass capacitor to function effectively. A storage unit stores the layout data that includes information used as a basis to calculate a first value corresponding to impedance between the power pin and the power source and a second value corresponding to
10 impedance between the power pin and the bypass capacitor. A calculation unit calculates the first value and the second value with use of the information. A judgment unit judges whether the layout allows the bypass capacitor to function effectively, by comparing the first value with the second
15 value. When a result of the judgment is negative, an output unit outputs error information.